

# Control of Process Plasmas for Next-Generation ULSI Devices

2008.12.17

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*Sony corporation*

# Outline

**1. Introduction**

**2. Plasma & Surface reaction models** (Yesterday)

**3. Current issues for CMOS fabrication** (Today)

**4. Requirement for “data base”** (Tomorrow)

**5. Summary**

# Requirement (Yesterday)

Plasma control  
for high aspect ratio  
contact hole etching

*Recipe*

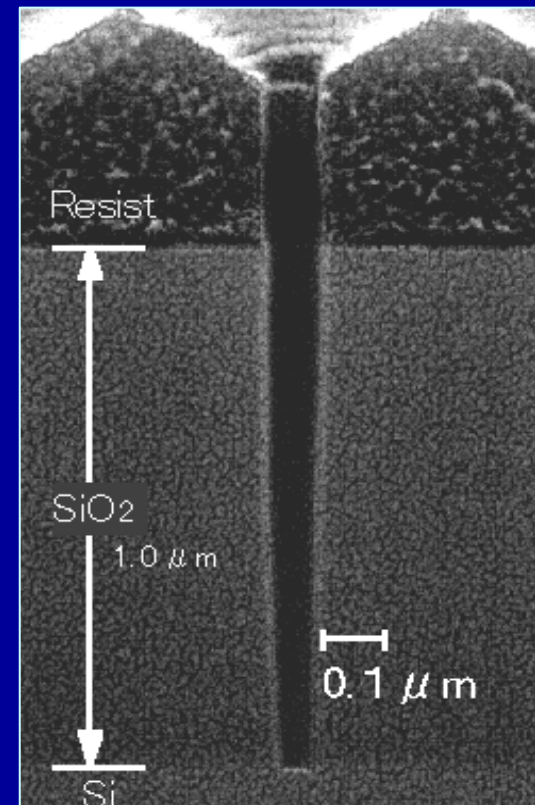
Power  
Pressure  
Gas flow rate  
...



Model ?

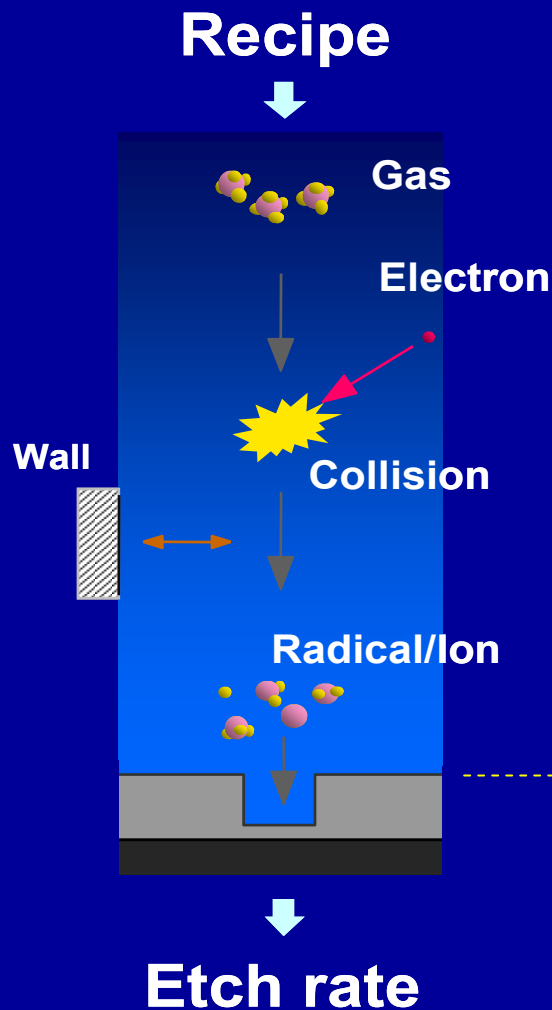
*Results*

Etch rate  
Selectivity  
Profile  
...



T. Tatsumi, JJAP 37 (1998) 2394

# Reactions



Electron heating

Dissociation

Plasma-wall interaction

Incident flux (radical/ion)

Plasma control

Surface control

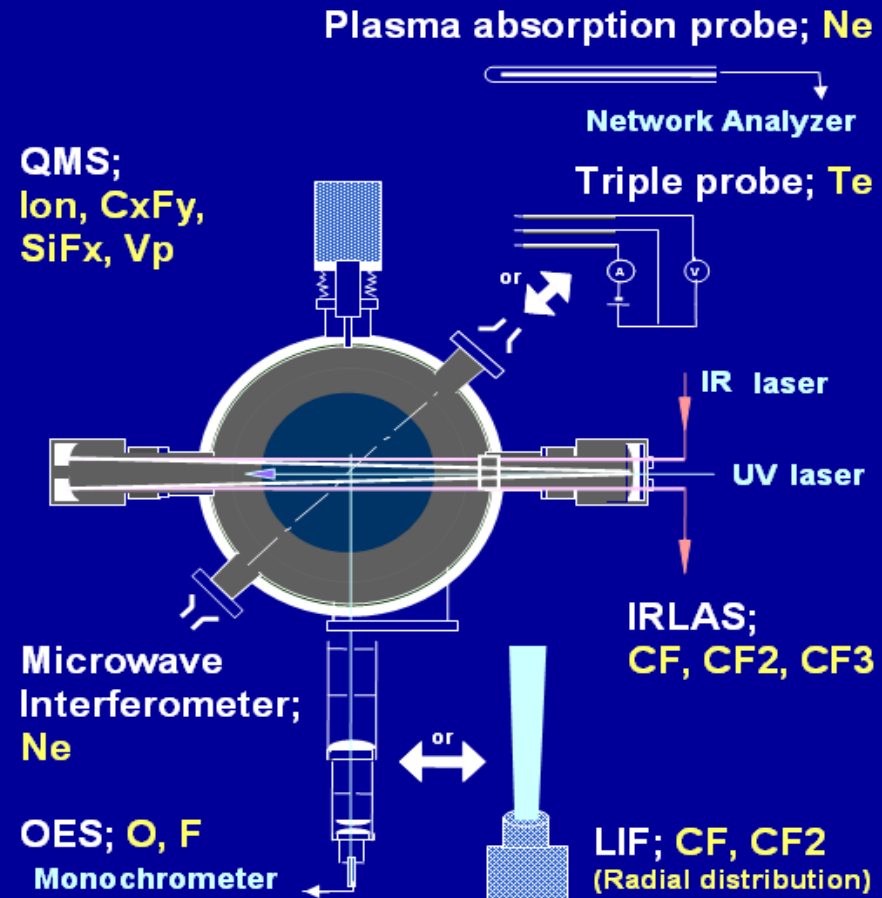
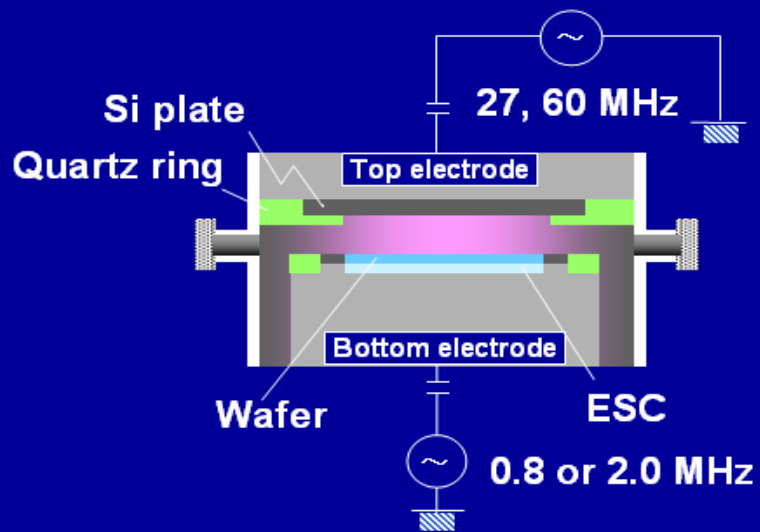
Transportation

Surface reaction

Etch rate

# In-situ monitoring of C-F plasma

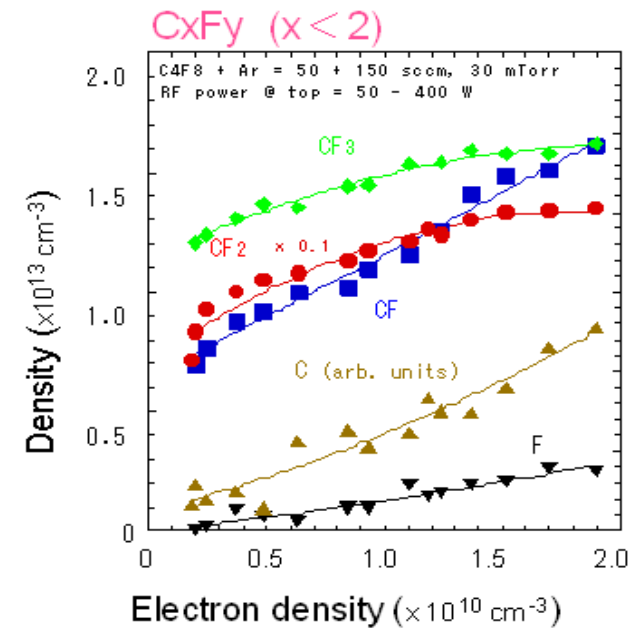
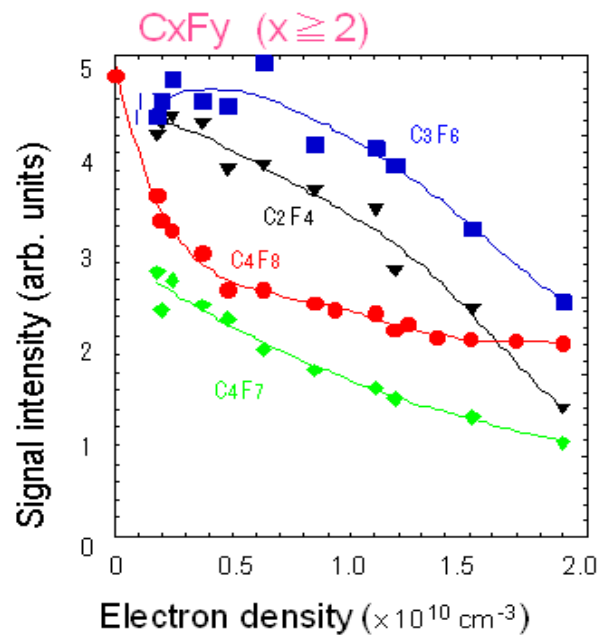
## Dual Frequency CCP C<sub>4</sub>F<sub>8</sub>/Ar/O<sub>2</sub>(N<sub>2</sub>)



In-situ monitoring of Ne, Te, ions and radicals

# Control of radical composition

H. Hayashi, JVST A 17 (5) (1999) 2557



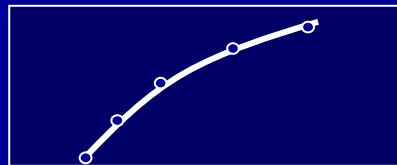
H. Hayashi et al., '98 SPP, 3A3-1



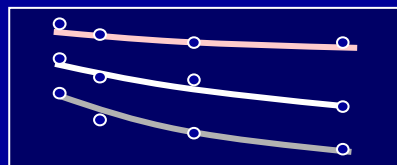
**Dissociation of fluorocarbon molecule**

# Dissociation of CF gases

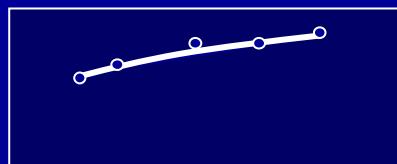
Dissociation degree of C4F8 (%)



Power



Flow rate



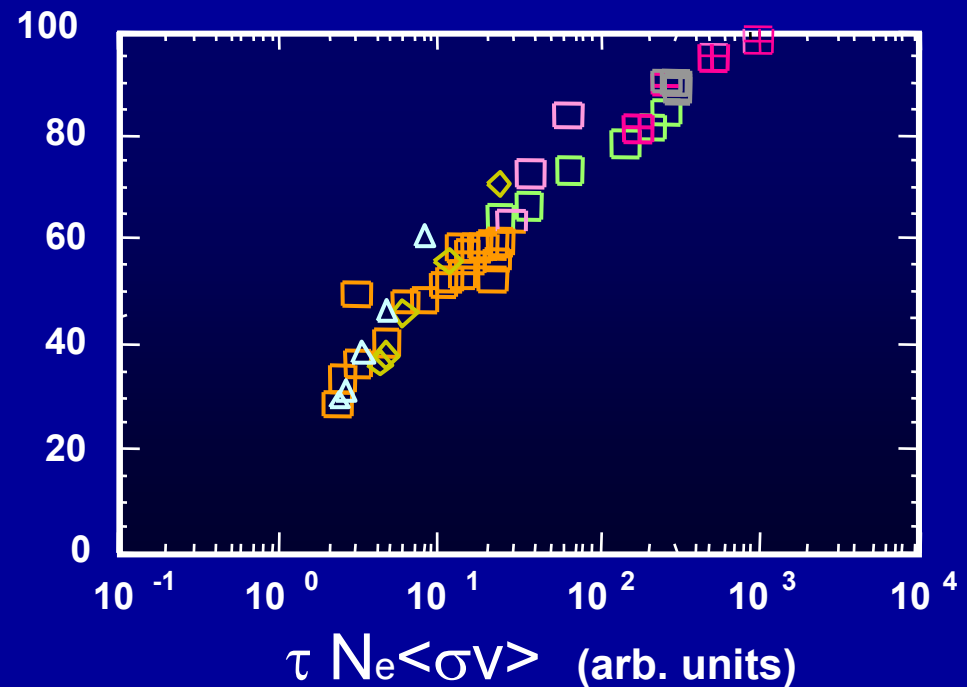
Pressure

## External parameters

Power: 100-2000W  
 Pressure: 20-100mTorr  
 Total flow: 100-600sccm  
 Gap: 17-27mm



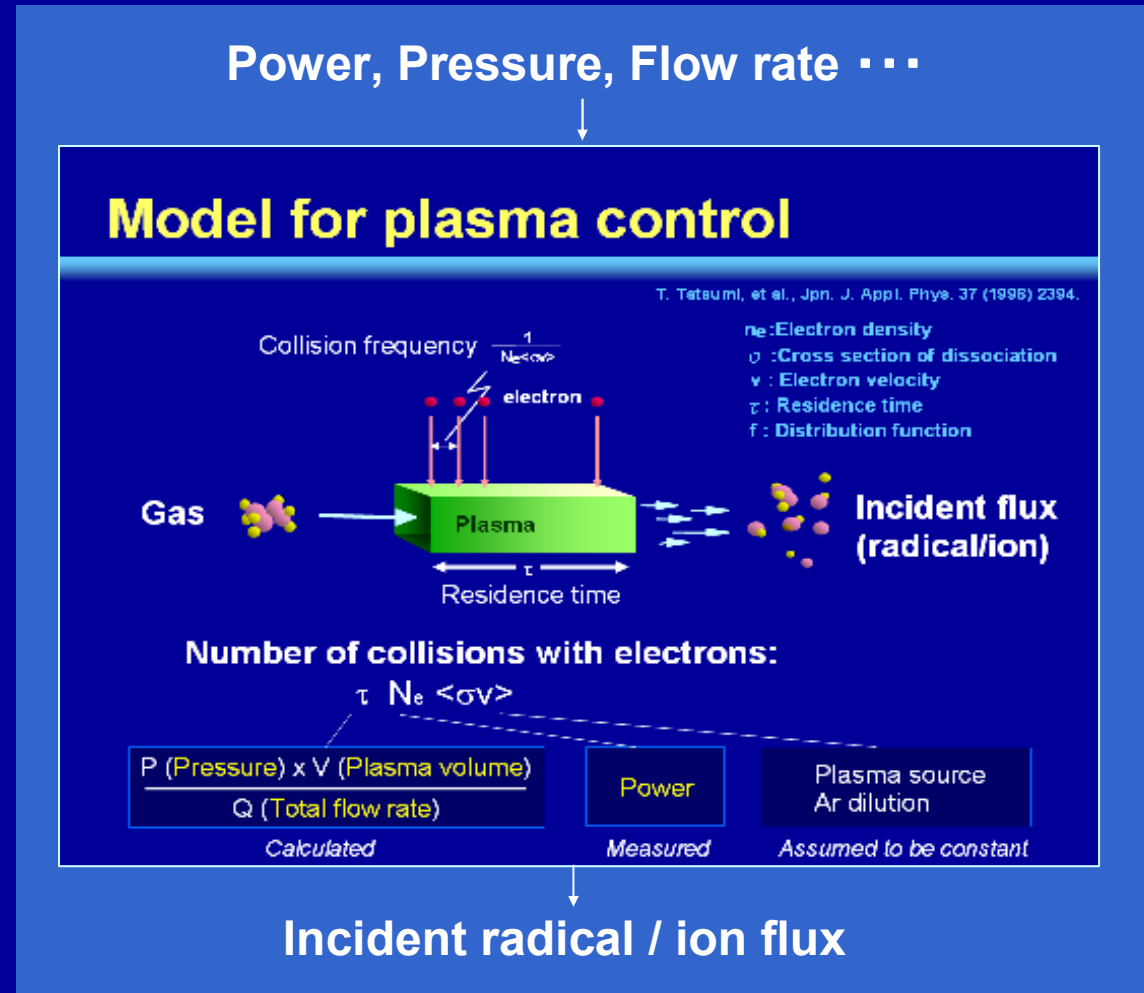
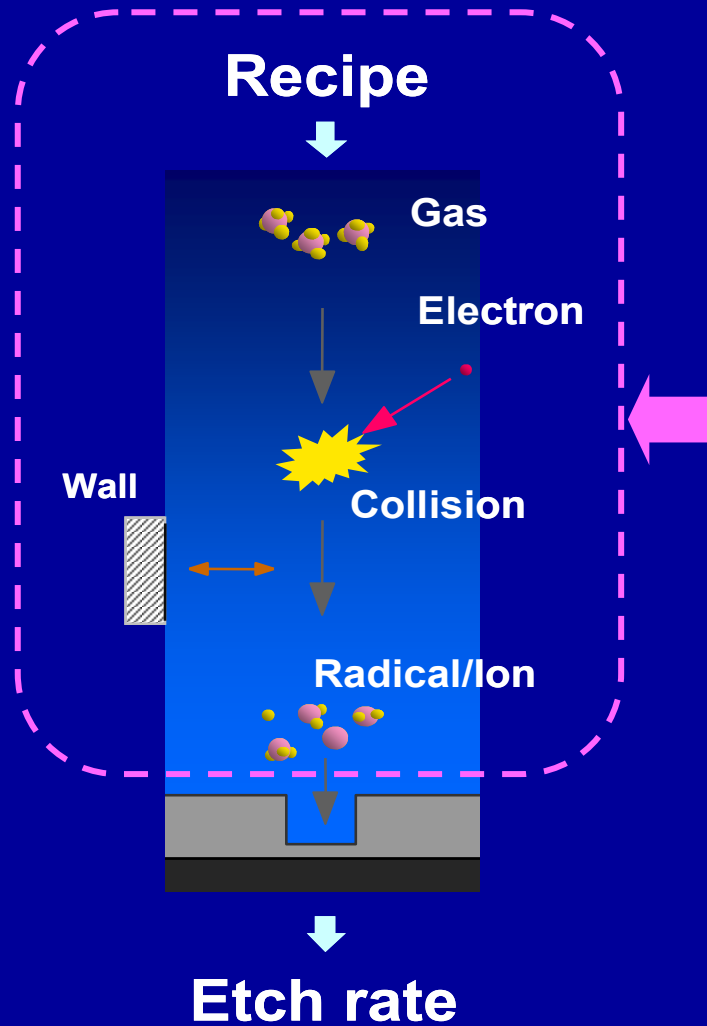
Dissociation degree of C4F8 (%)



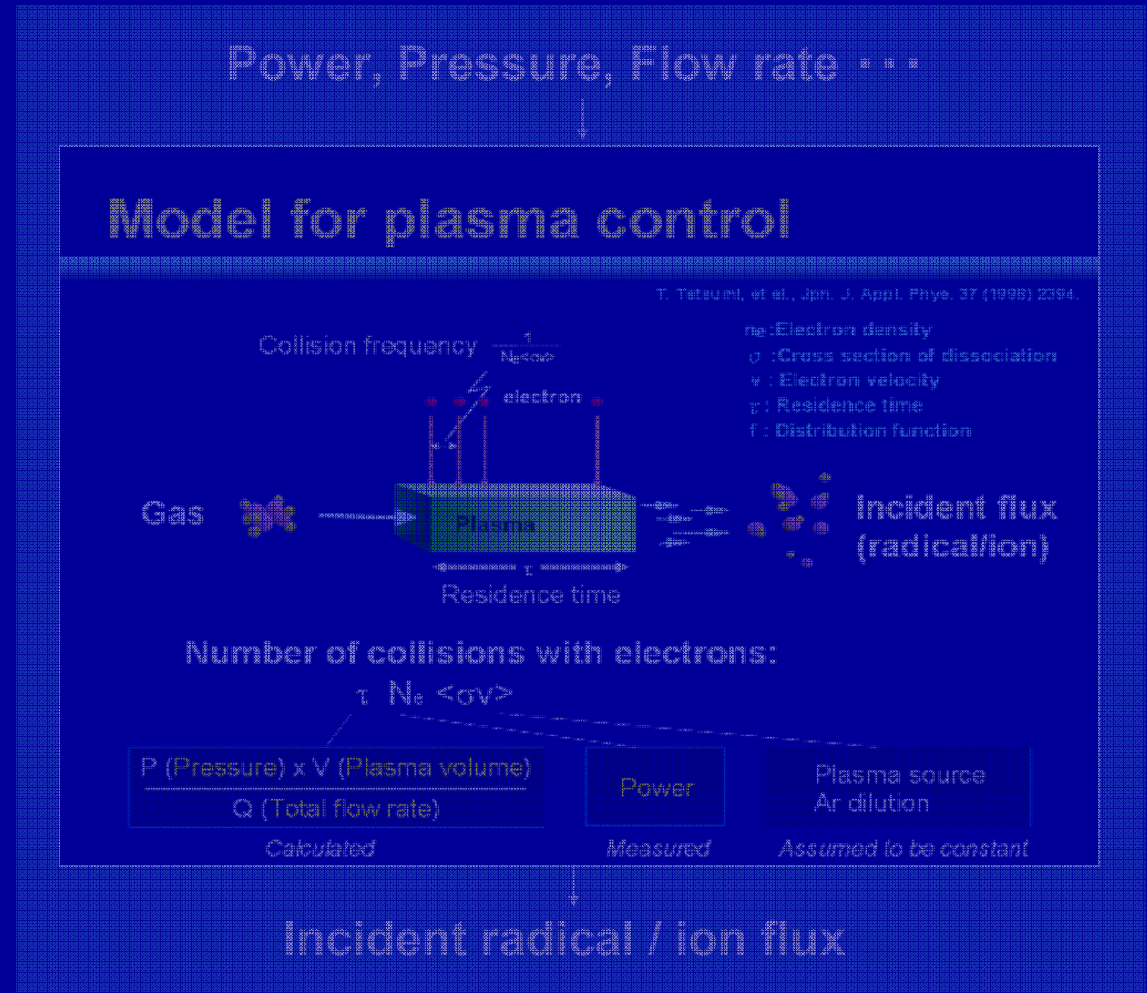
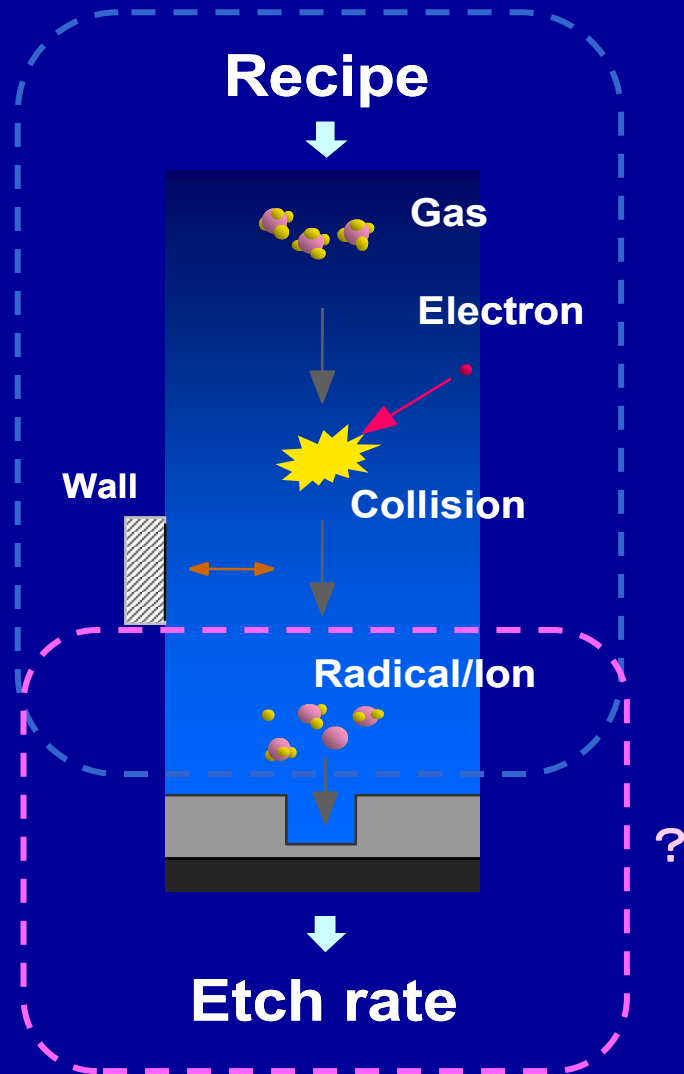
## Number of collisions

$\tau$  → residence time (4-24ms)  
 $N_e$  → electron density ( $2e9-11cm^{-3}$ )  
 $\langle\sigma v\rangle$  → electron energy (3-5eV)

# External parameters → Incident fluxes

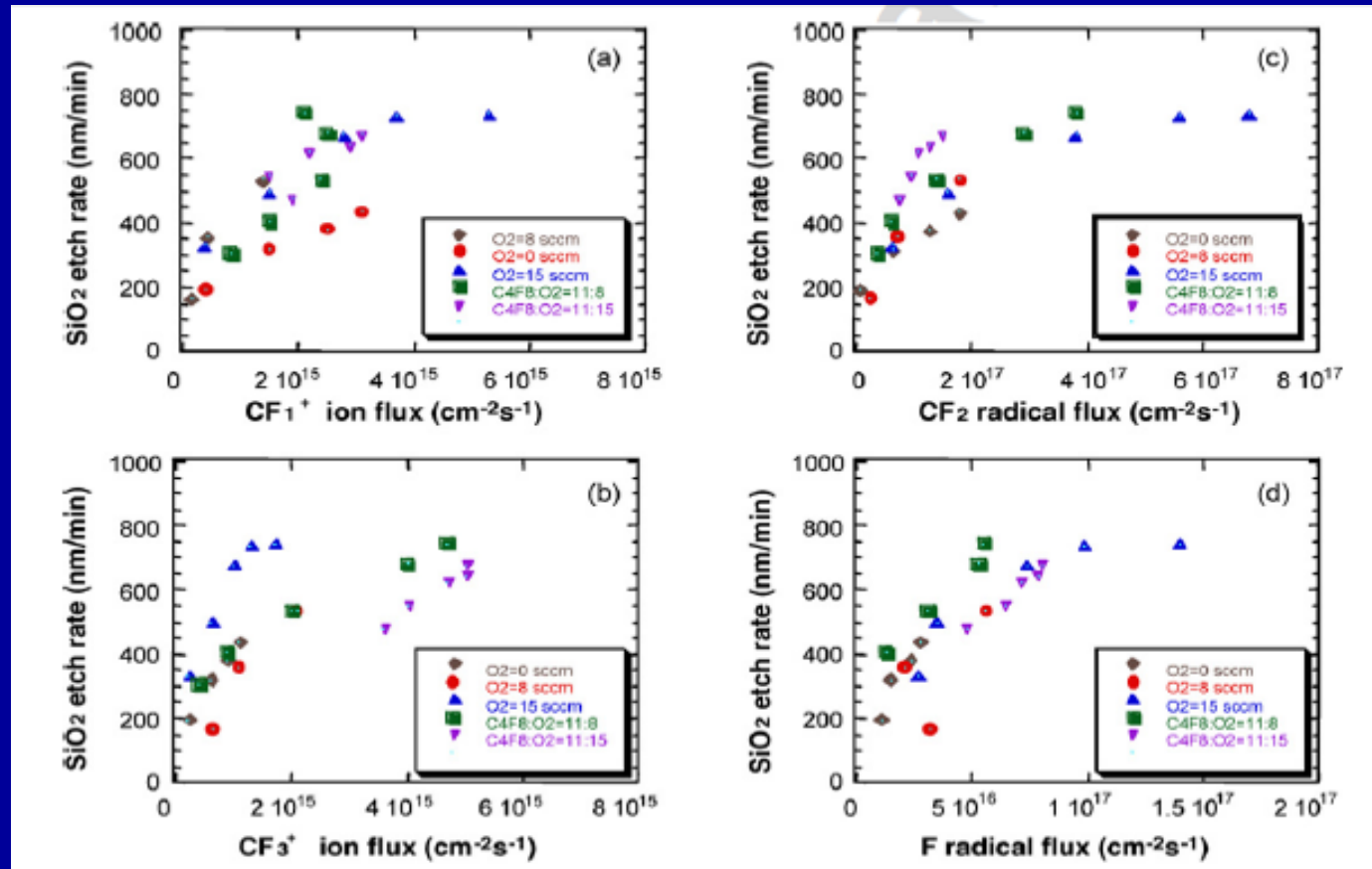


# Incident fluxes → Etch rate

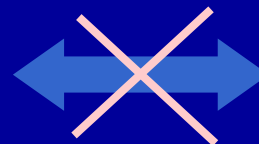


# Incident fluxes → Etch rate

T. Tatsumi, Applied Surface Science 253(2007) 6716



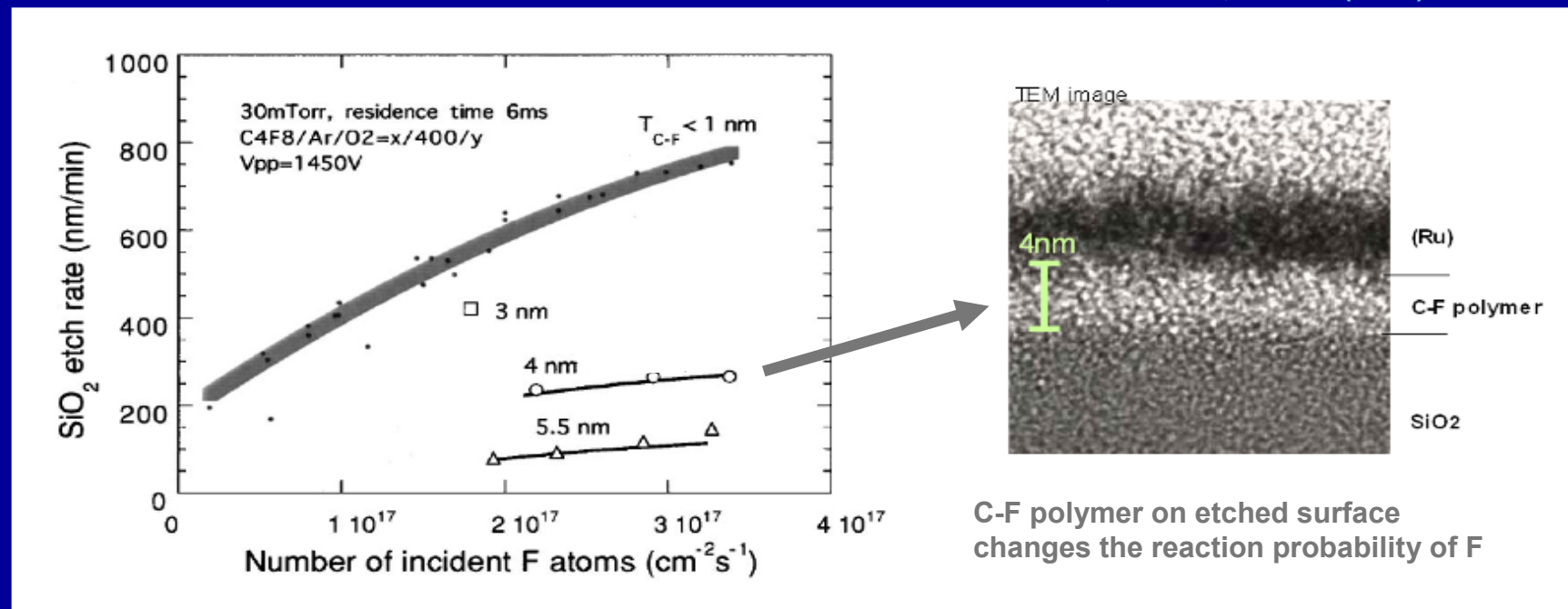
CF<sup>+</sup>, CF<sub>3</sub><sup>+</sup>, CF<sub>2</sub>, F, ...



SiO<sub>2</sub> etch rate

# Incident fluxes → Etch rate

T. Tatsumi et al., JVST B, Vol. 18 (2000) 1897



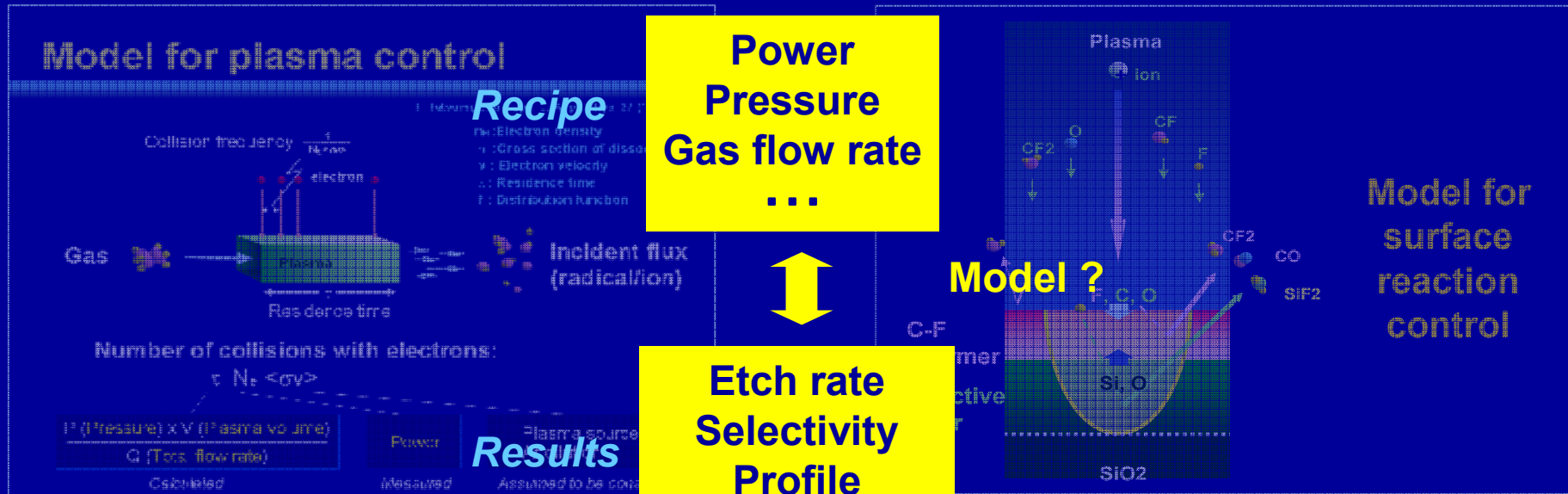
Total F in all incident CF<sub>x</sub>  
radical and/or ion fluxes

x

Reaction probability  
(ion energy and polymer thickness)

↔ SiO<sub>2</sub> etch rate

# Etch rate prediction



*Dissociation of C<sub>4</sub>F<sub>8</sub>*

*Bias power & C<sub>4</sub>F<sub>8</sub>/O<sub>2</sub> ratio*

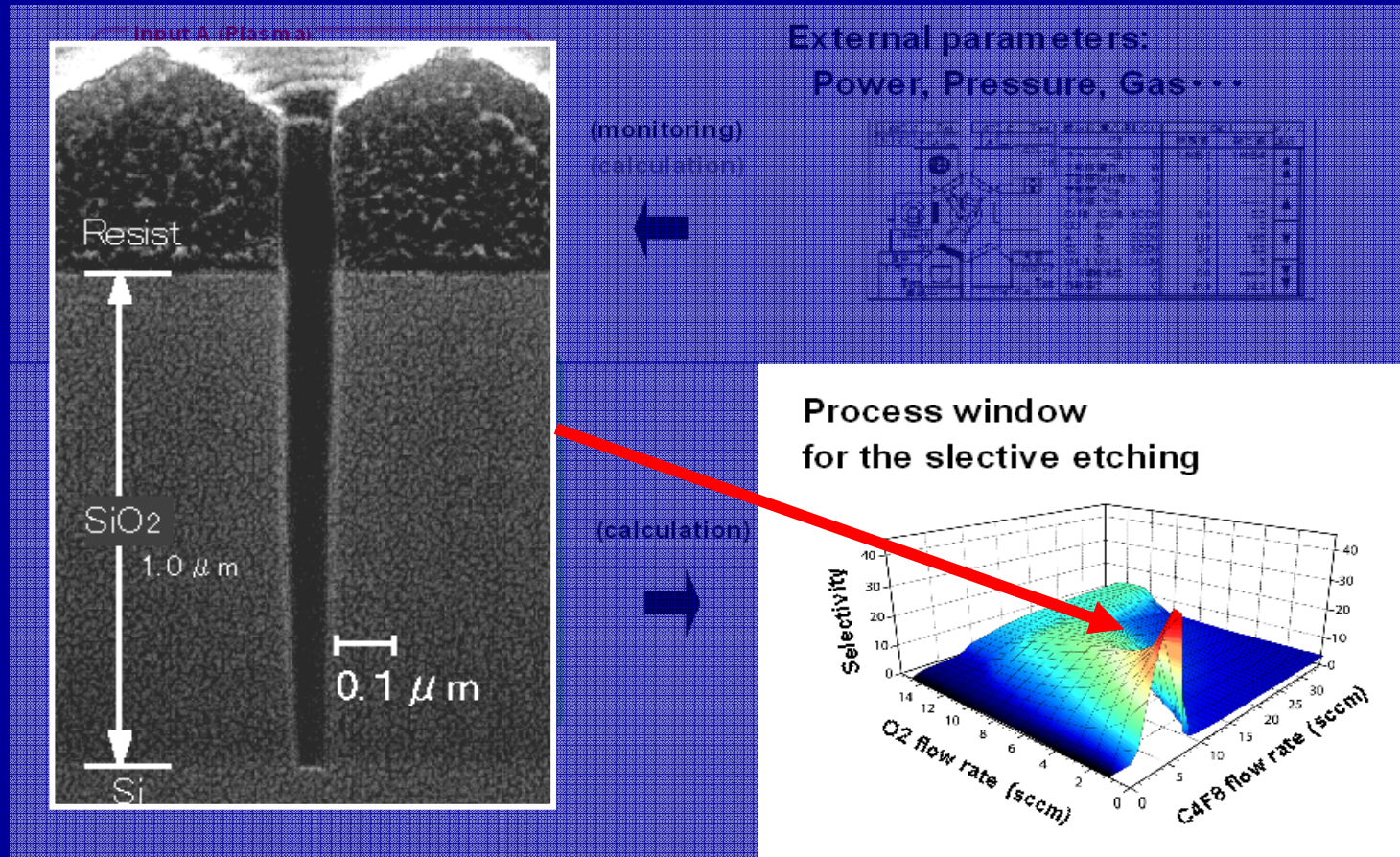
**Total F in all incident CF<sub>x</sub> radical and/or ion fluxes**

**x**

**Reaction probability (ion energy and polymer thickness)**

**SiO<sub>2</sub> etch rate**

# Etch rate prediction



Plasma + Surface reactions → Process performance

# Outline

1. Introduction

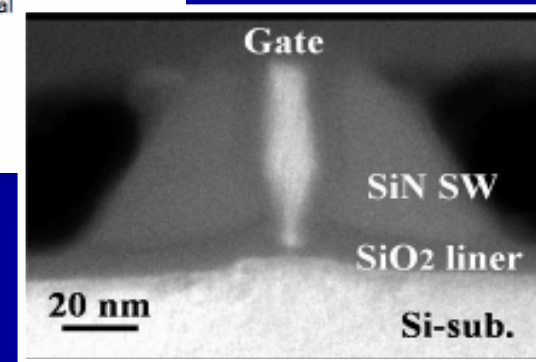
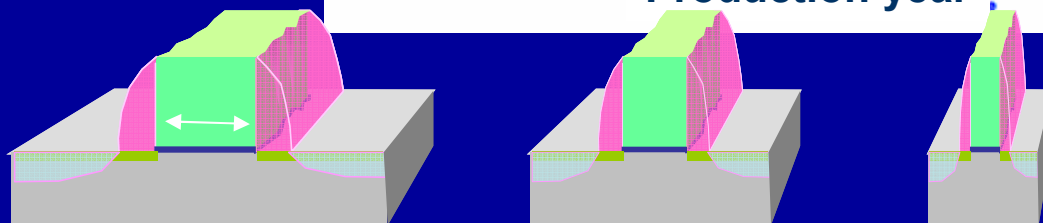
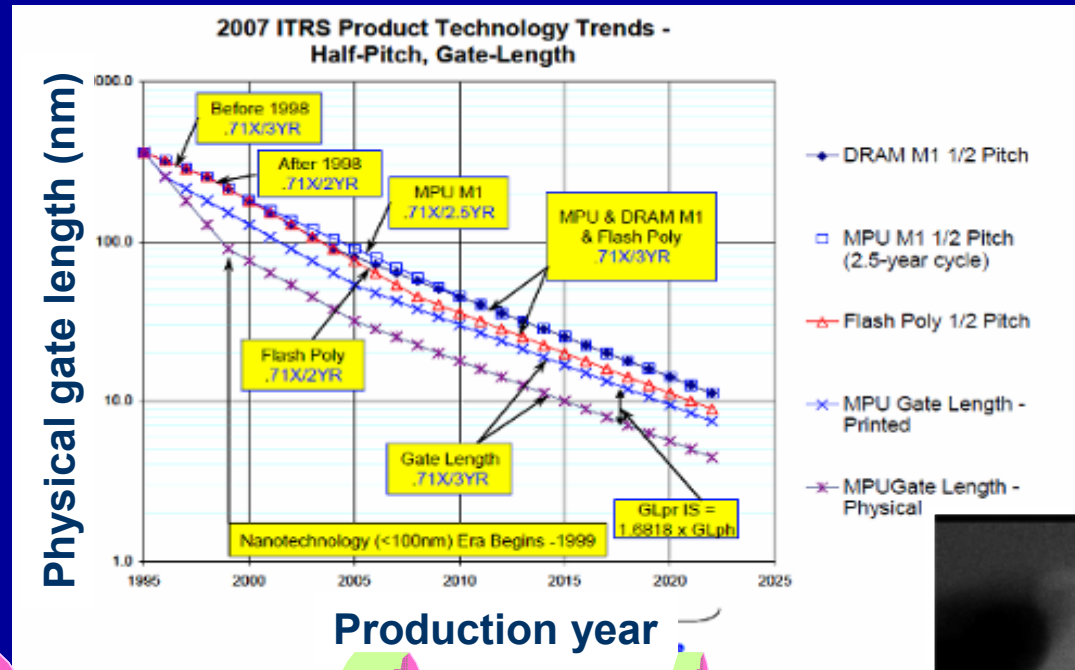
2. Plasma & Surface reaction models (Yesterday)

3. **Current issues for CMOS fabrication** (Today)

4. Advanced process control (Tomorrow)

5. Summary

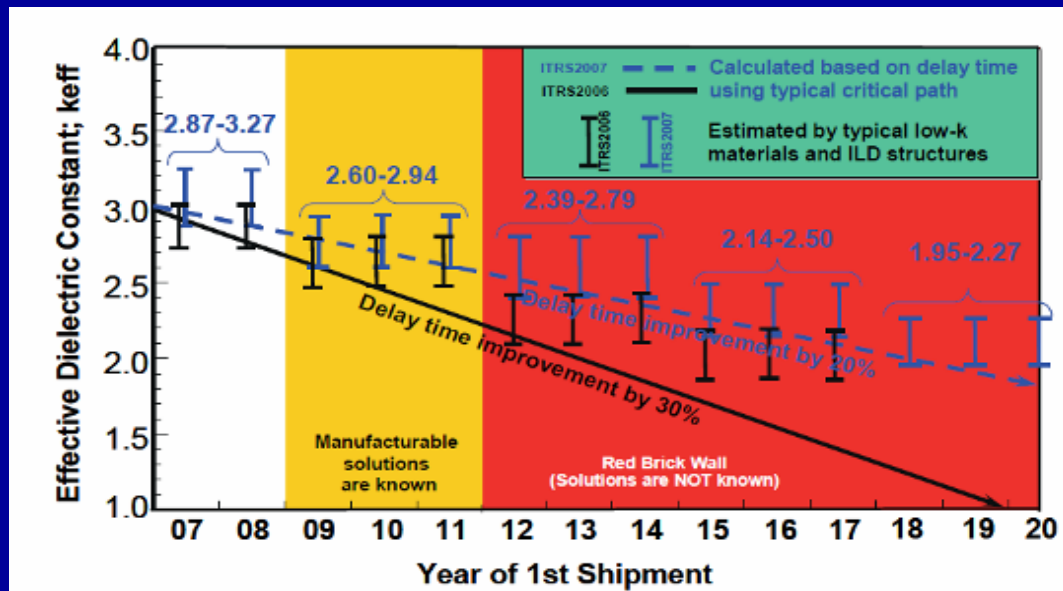
# ITRS roadmap 2007



Wakabayashi, 2004 IEDM

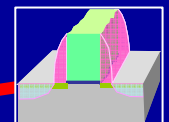
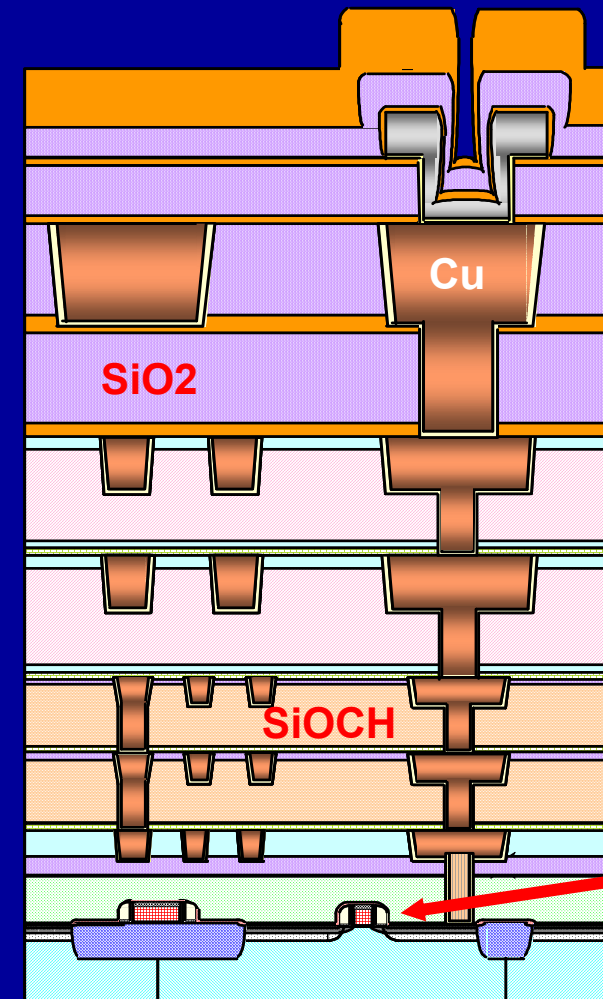
**Gate length → 10nm ± 1nm @2015**

# ITRS roadmap 2007



**SiOCH (low-k materials)**

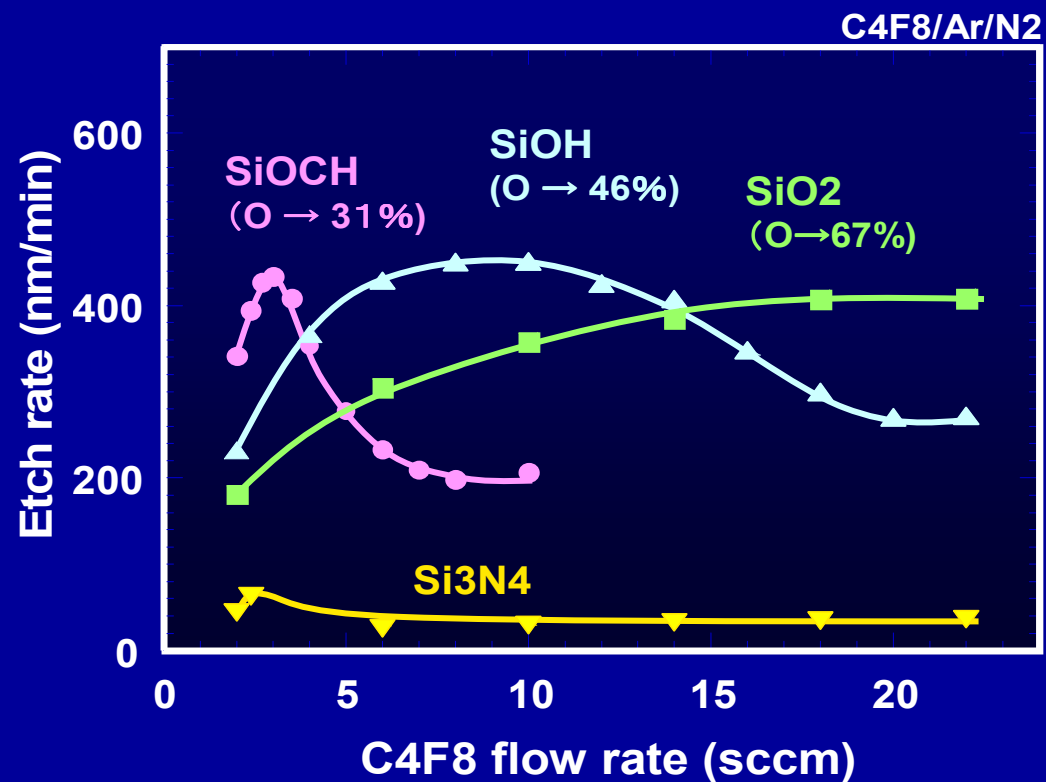
→ Reduction of capacitance between Cu wiring



# Small process window

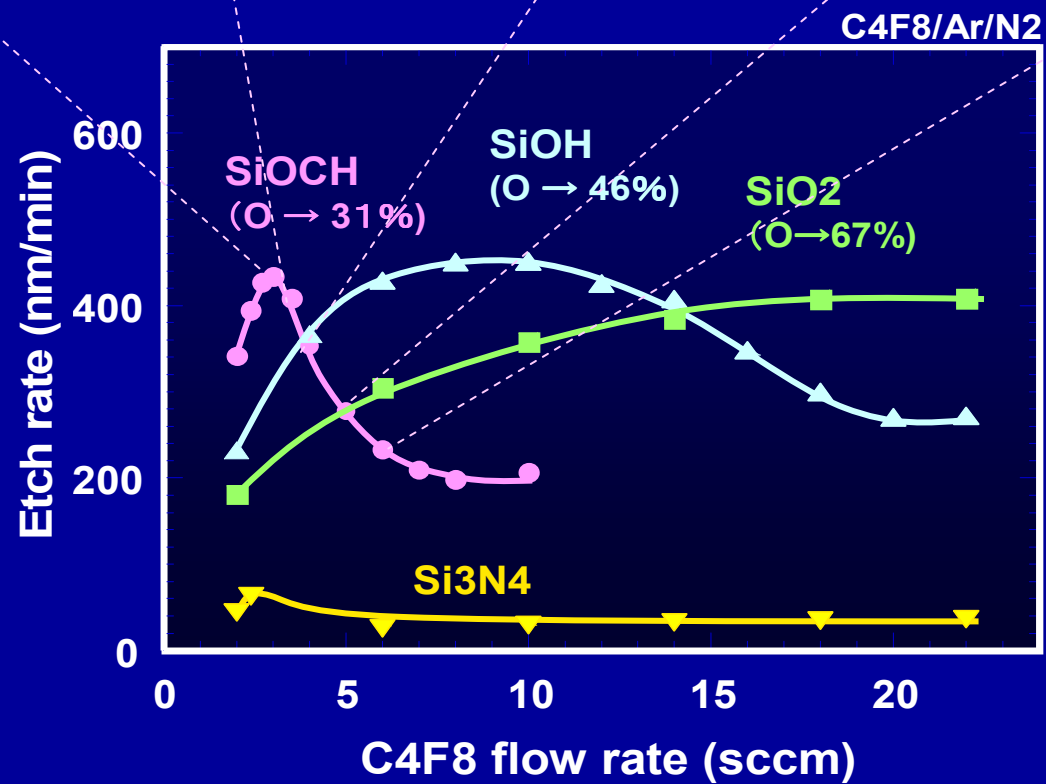
Decrease in O concentration in SiOCH film

→ Etch rate is sensitive to change in C<sub>F</sub>x densities.



T. Tatsumi et. al.,  
JVST A 23 (2005) 938

# Small process window



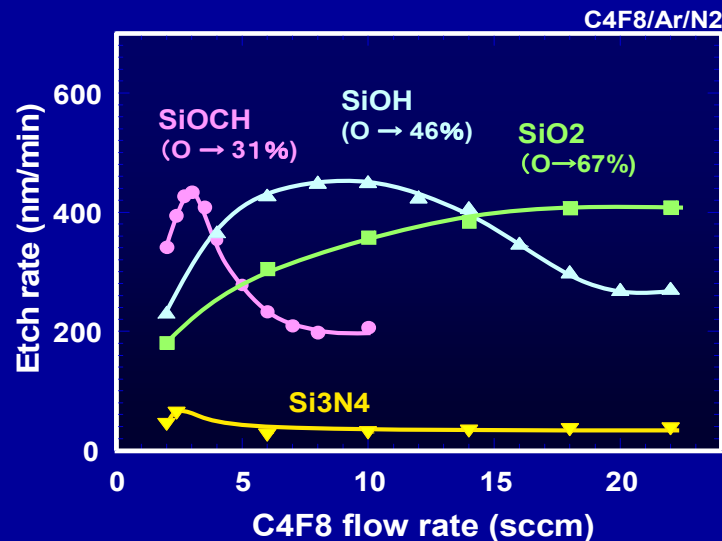
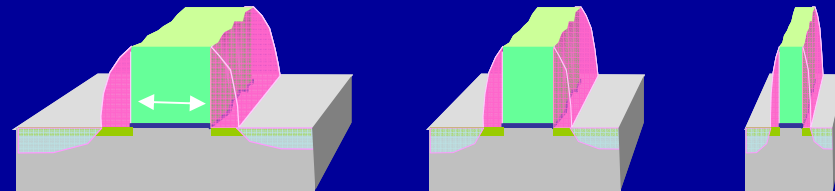
T. Tatsumi et. al.,  
JVST A 23 (2005) 938

# Requirements (Today)

Small size

→ Atomic layer control

$\Delta CD < 1\text{nm}$  ?



New materials (Low-k, High-k)

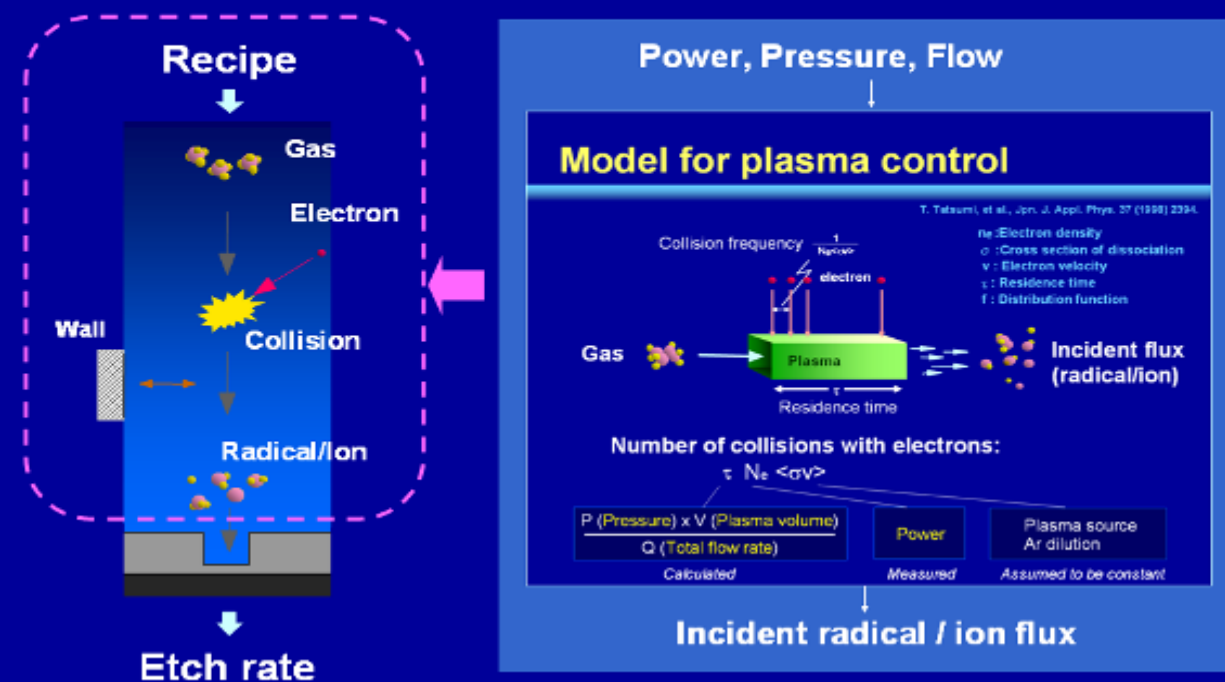
→ Narrow process window

**Complete suppression of fluctuation**

# Plasma wall interaction

2008.08.16, T. Tatsumi

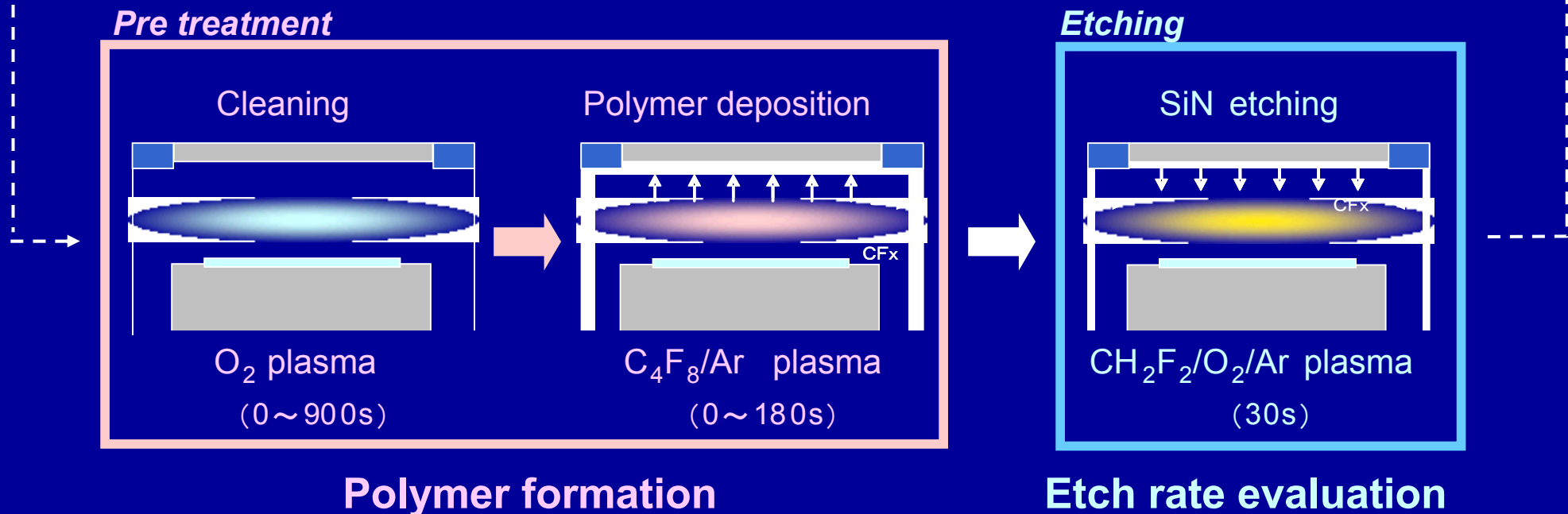
## External parameters → Dissociation



Effect of wall conditions on etch rate ?

# Experiment

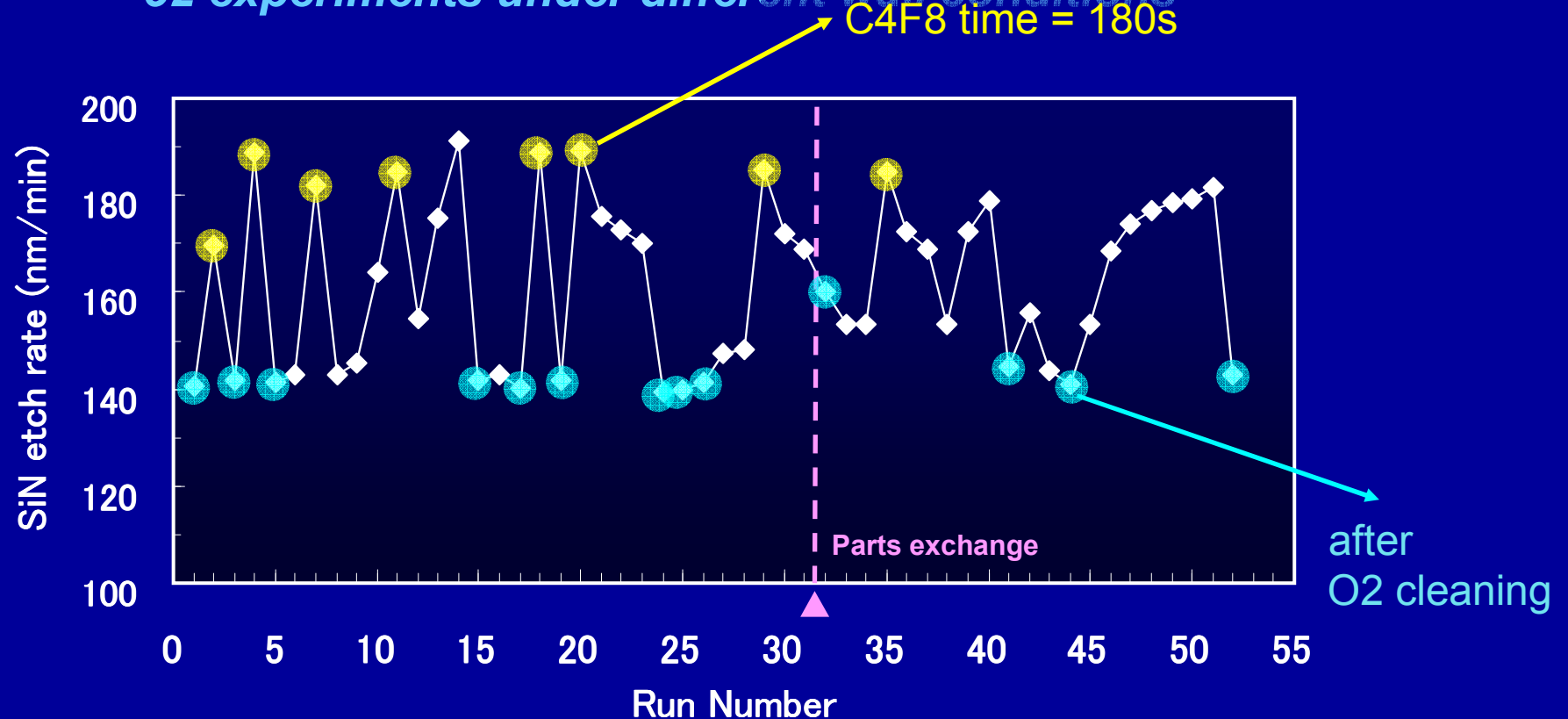
*52 experiments under different wall conditions*



**Effect of wall conditions on etch rate ?**

# Fluctuation of SiN etch rate

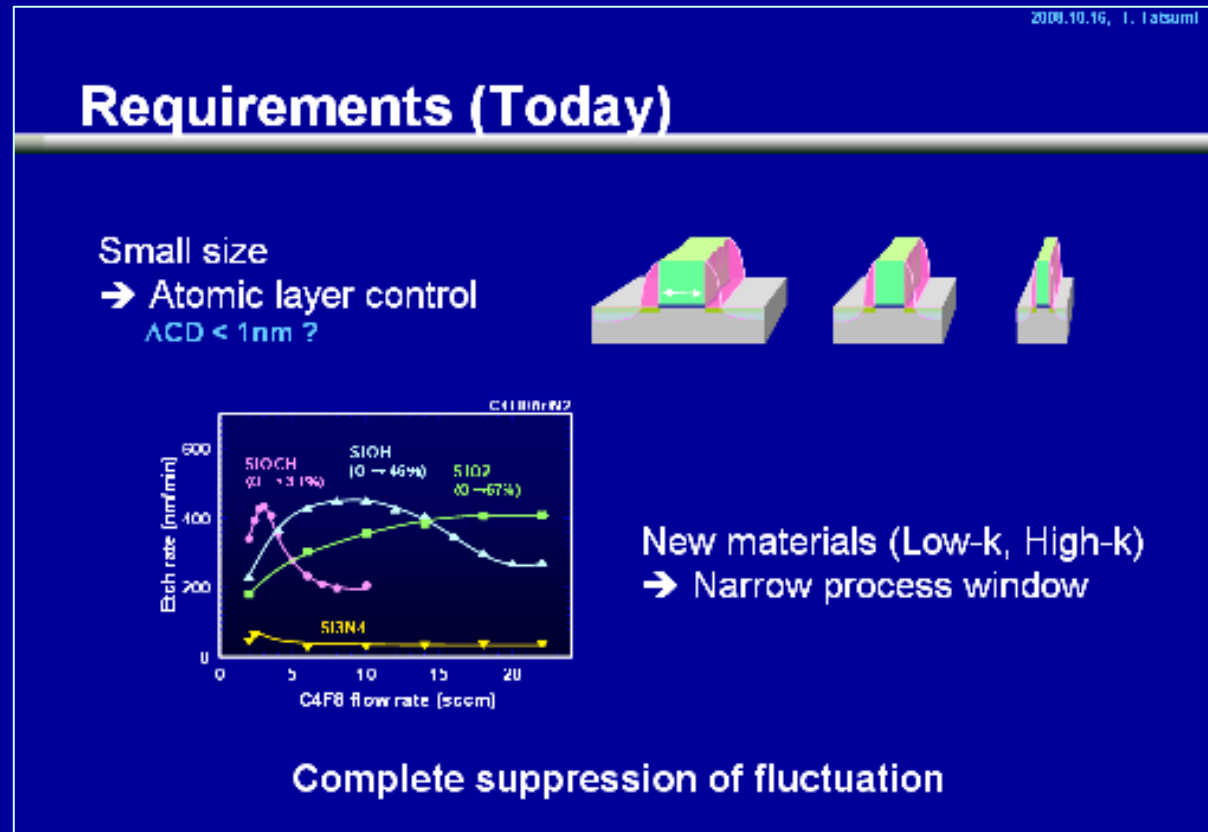
*52 experiments under different wall conditions*



**The same external parameters, but different etch rate.**

*Polymer, oxidation of electrode, parts erosions, wafer...*

# “Noise factor”



Requirement for  
stable plasma



Dissociation ⇔ external parameters

(control factor)

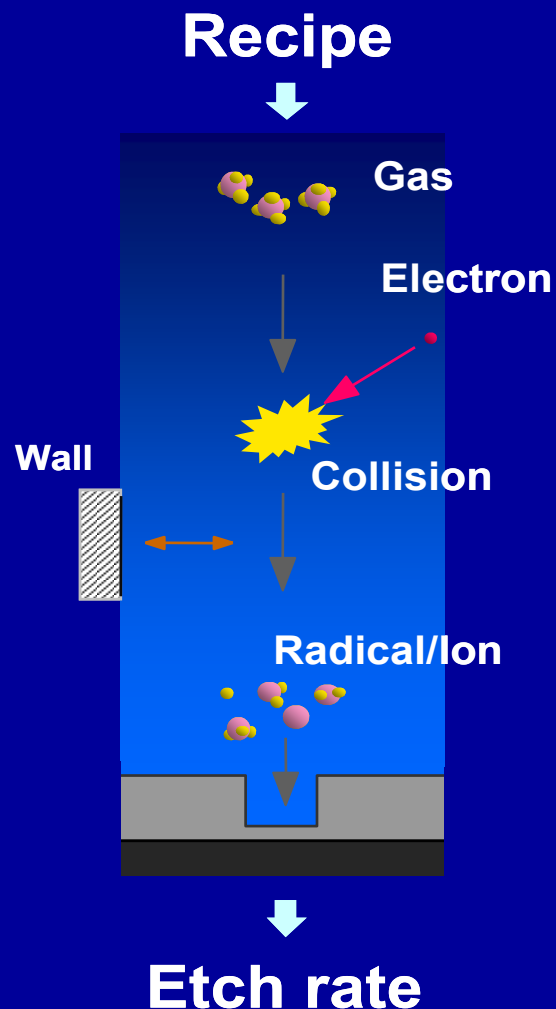
Wall reaction ⇔ history of plasma treatment

(noise factor)

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1. Introduction
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4. Requirement for “data base” (Tomorrow)
5. Summary

# Data base for quantitative simulation



## A: Plasma

Collision cross sections  
for actual process plasma  
(dissociation, ionization)

## B: Wall

Sticking probabilities  
on various materials

## C: Wafer surface

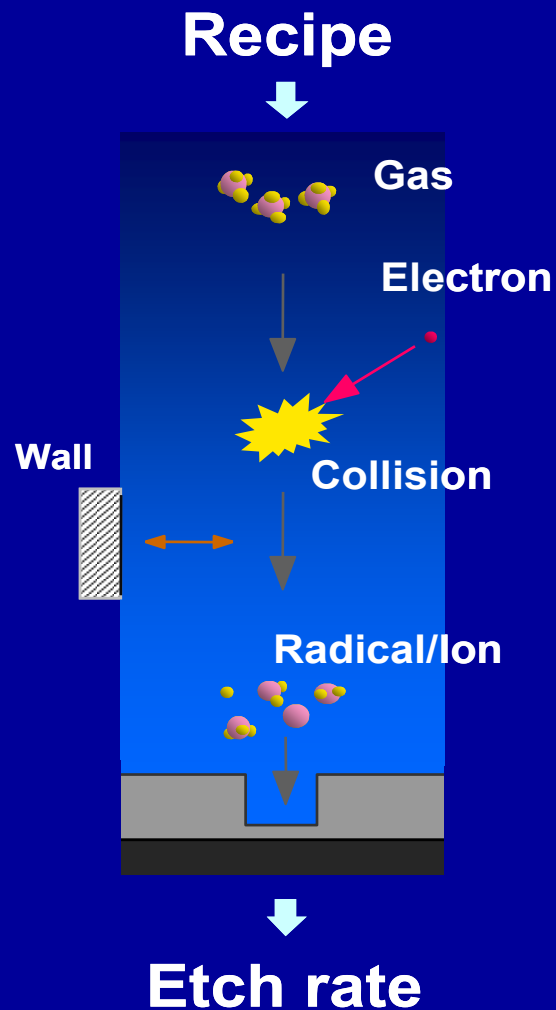
Reaction probabilities  
Etch yields

# Collision cross section

Materials	Gas
Si	SF <sub>6</sub> , Cl <sub>2</sub> , HBr, O <sub>2</sub> , Ar, He, N <sub>2</sub> , NF <sub>3</sub> , SiF <sub>4</sub>
SiO <sub>2</sub>	CF <sub>4</sub> , CHF <sub>3</sub> , CH <sub>2</sub> F <sub>2</sub> , CH <sub>3</sub> F, C <sub>4</sub> F <sub>6</sub> C <sub>4</sub> F <sub>8</sub> , C <sub>5</sub> F <sub>8</sub> , CO, O <sub>2</sub> , N <sub>2</sub> , Ar
Al	Cl <sub>2</sub> , BCl <sub>3</sub> , HCl, Ar
Organic polymer	O <sub>2</sub> , H <sub>2</sub> , N <sub>2</sub> , CO, NH <sub>3</sub> , Ar, He, H <sub>2</sub> O

$\sigma$  for Ionization, dissociation and optical emission.

# Data base for quantitative simulation



## A: Plasma

Collision cross sections  
for actual process plasma  
(dissociation, ionization)

## B: Wall

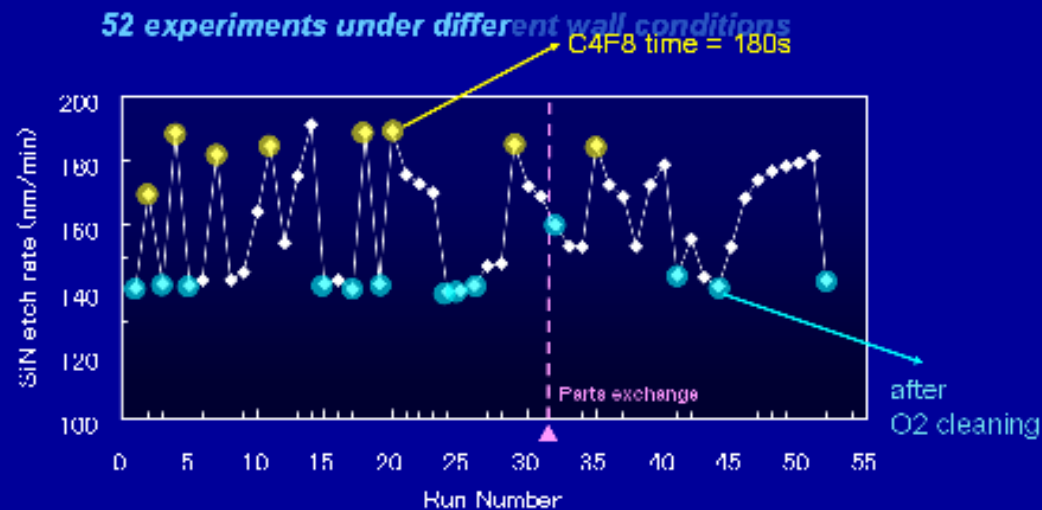
Sticking probabilities  
on various materials

## C: Wafer surface

Reaction probabilities  
Etch yields

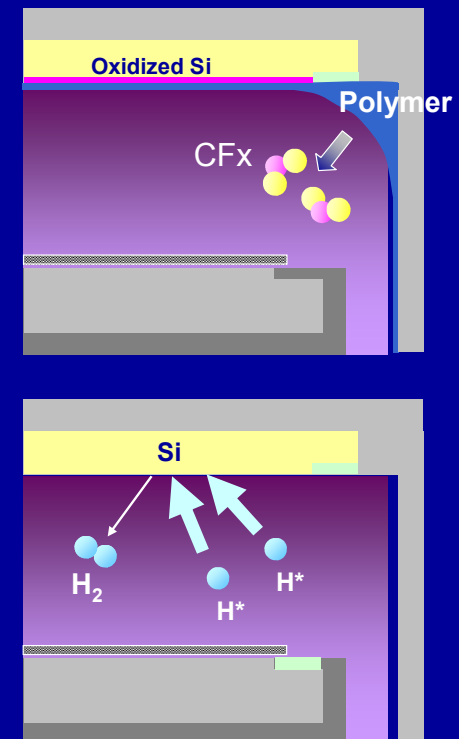
# Sticking coefficient

## Fluctuation of SiN etch rate



The same external parameters, but different etch rate.

*Polymer, oxidation of electrode, parts erosions, wafer...*



Etch rate sensitively depends on wall conditions

# Sticking coefficient

- Slide2 -

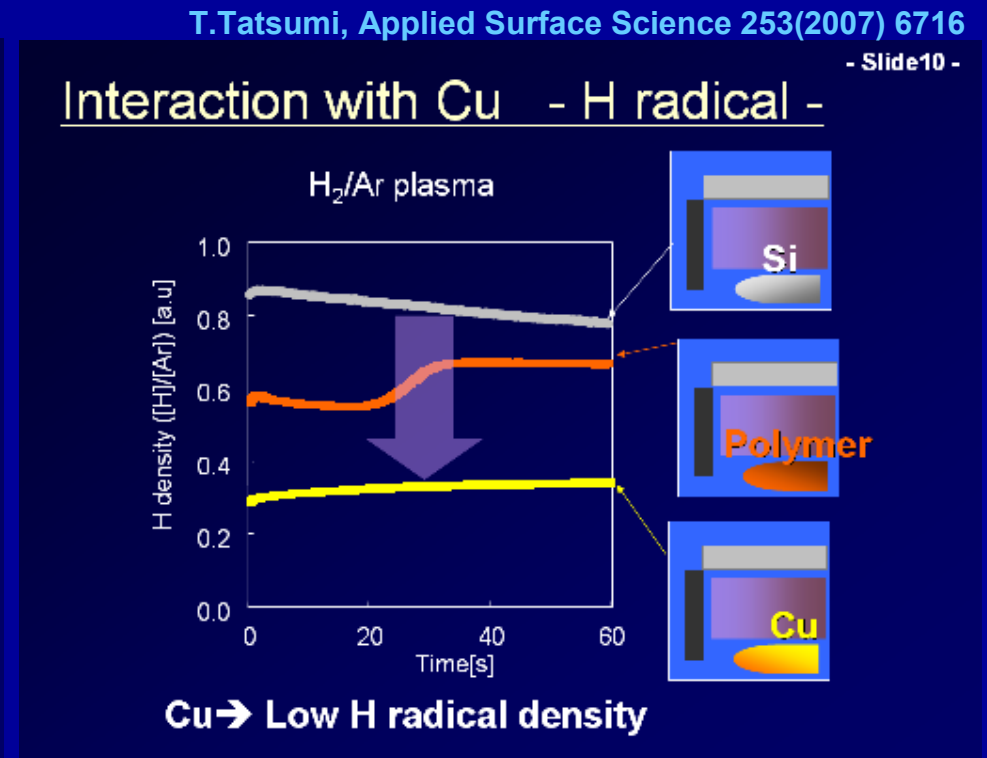
## Radical control

① Generation

- Electron-impact dissociation
- Ex) Hydrogen
- $H + e \rightarrow H^* + e$
- $H_2 + e \rightarrow H + H^* + e$
- ⋮

② Loss

- Reaction with wafer
- **Reaction with wall**
- Collision to other species
- Exhaust

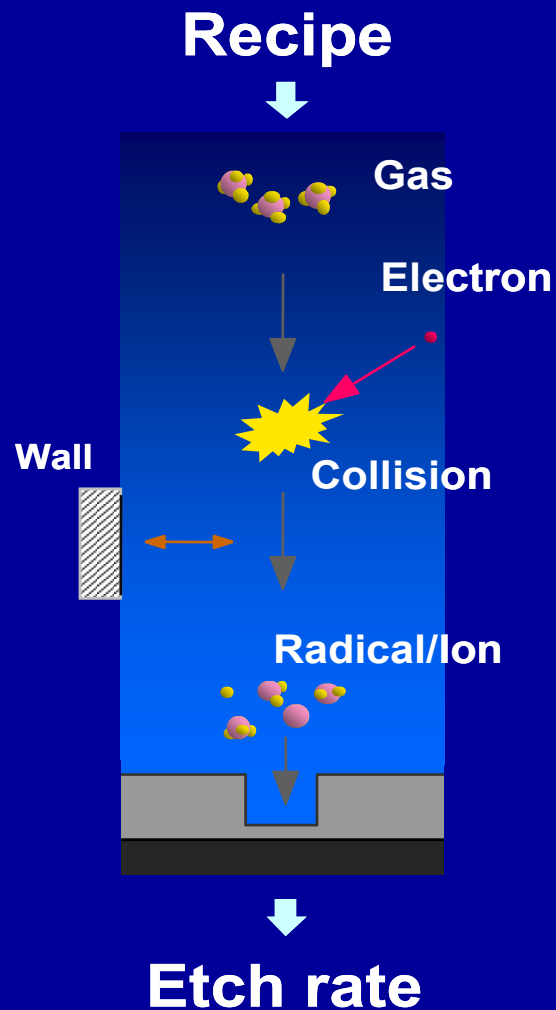


## Generation and loss on chamber walls

Materials: Si, SiO<sub>2</sub>(Quartz), Y<sub>2</sub>O<sub>3</sub>, Al<sub>2</sub>O<sub>3</sub>, C-F polymer, SiBr<sub>x</sub>O<sub>y</sub>...

Conditions: Temperature, Ion energy, conductivity, roughness, UV...

# Data base for quantitative simulation



## A: Plasma

Collision cross sections  
for actual process plasma  
(dissociation, ionization)

## B: Wall

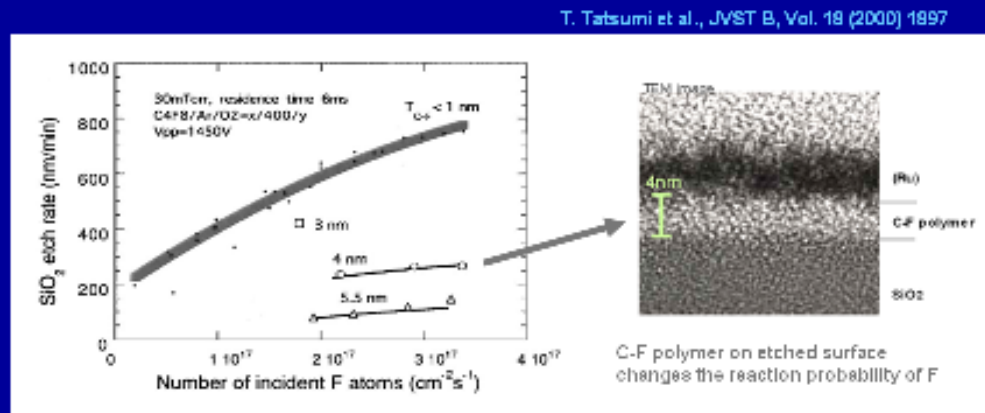
Sticking probabilities  
on various materials

## C: Wafer surface

Reaction probabilities  
Etch yields

# Reaction probability

## Incident fluxes → Etch rate

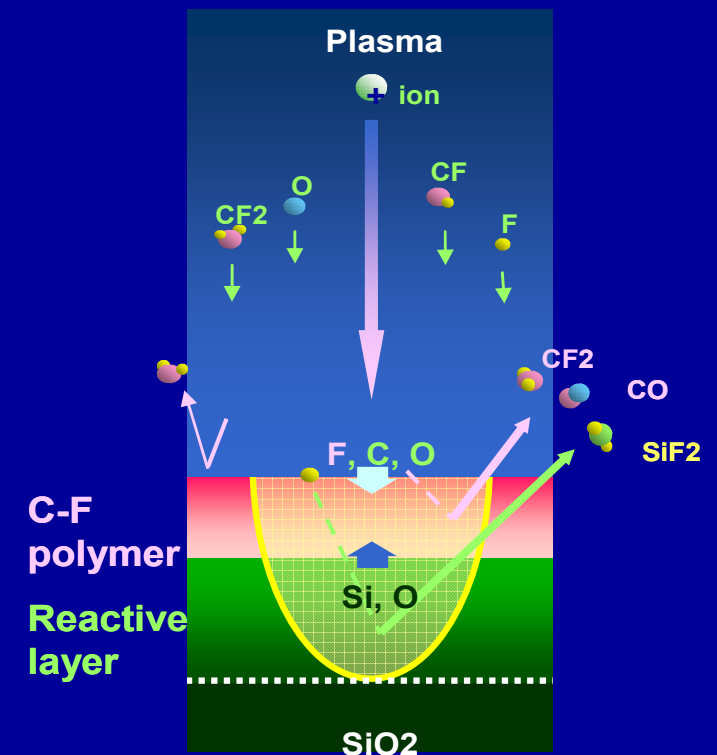


Total F in all incident  $\text{CF}_x$  radical and/or ion fluxes

x

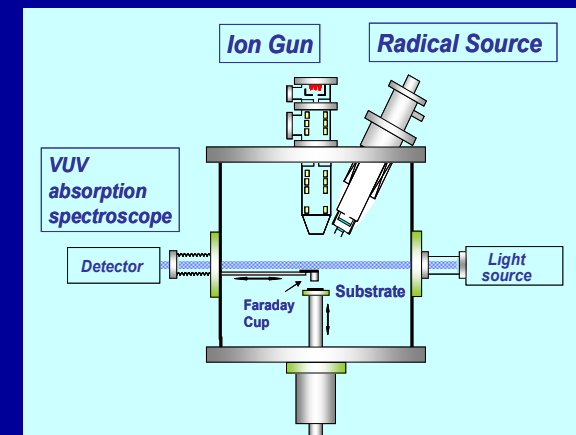
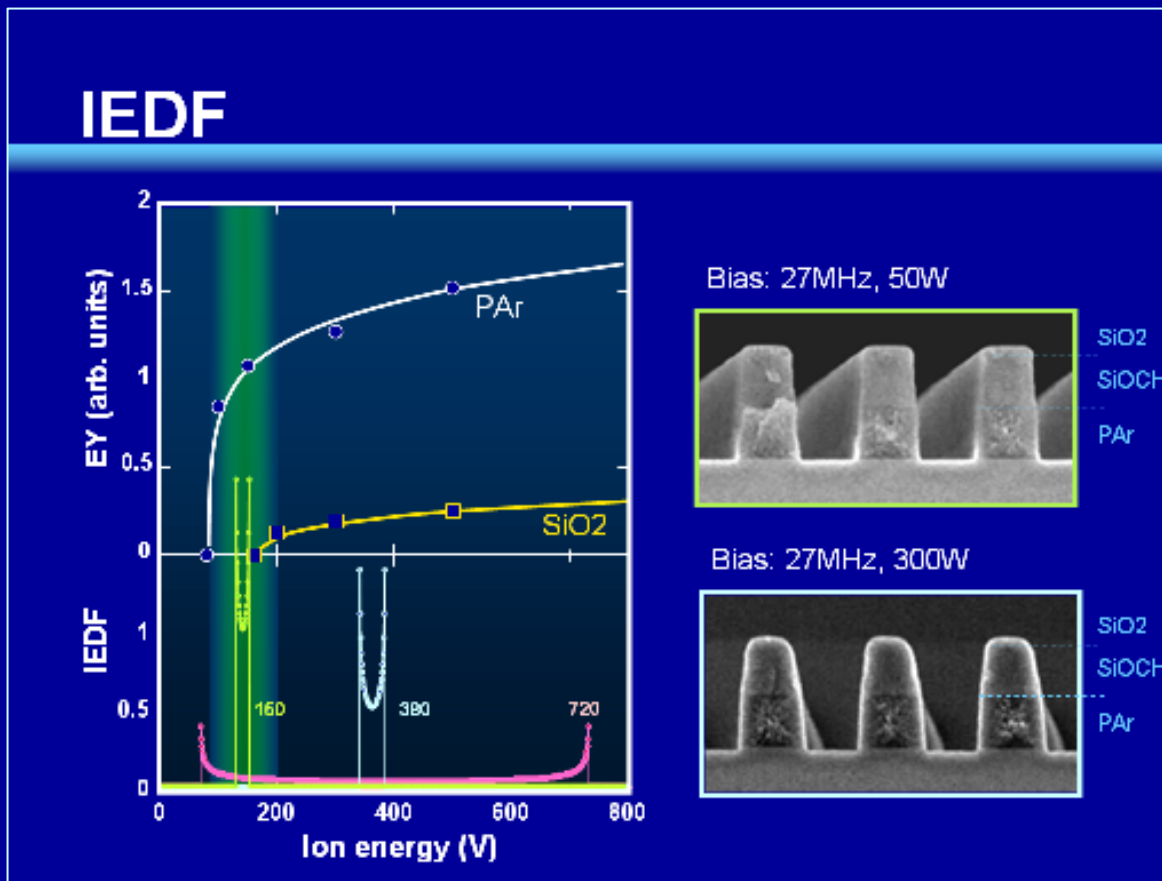
Reaction probability  
(ion energy and polymer thickness)

↔  $\text{SiO}_2$  etch rate



Etch rate depends on “incident flux” and “reaction probability”

# Reaction probability



Etching yield depends on temperature, ion energy, .....

# Summary

**In fabrication of “near future” Si-CMOS devices,**

- 1. Fluctuation of pattern width and damage must be suppressed within several atomic layers.**
- 2. Quantitative reaction models, monitoring technologies, simulation technologies and feed back control systems are required.**
- 3. Data base of collision cross sections, sticking coefficient on chamber walls, and reaction probabilities on wafers.**